

Time-Table M.Tech. IInd Semester Mid Term-1 Examination, November 2021

Time: Shift I: 03:00 - 3:40 PM
Shift II : 04:00 - 4:40 PM

| <u>Date</u> | <u>Shift</u> | <u>VLSI Design (EC)</u> |
|-------------|--------------|---|
| 30.10.2021 | I | Modeling & Simulation PGVLSI-201 |
| | II | Advanced microprocessors & Microcontrollers PGVLSI-202 |
| 01.11.2021 | I | VLSI Technology PGVLSI-203 |
| | II | Introduction of Internet of Things PGVLSI-204 |
| 02.11.2021 | I | Wireless Networks PGVLSI-205 |

Jai
26/10/2021
Jai Kumar Sharma
Centre Supdt. - SOET (Exam.)

Dr. Ranjeet Singh Tomar
26/10/2021
Dr. Ranjeet Singh Tomar
Dean (SOET)